



4-19-02 2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kim, et al.

Docket No: TI-29012

Serial No: 09/574,653

Examiner: E. Ortiz

Conf. No: 8503

Art Unit: 2815

Filed: 05/18/2000

For: A TUNABLE SIDEWALL SPACER PROCESS FOR CMOS INTEGRATED CIRCUITS

LETTER TO OFFICIAL DRAFTSPERSON

Assistant Commissioner For Patents
Washington, DC 20231

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 10-26-01.

Ann Trent
Ann Trent

Dear Sir:

Transmitted herewith for filing is one page of formal drawings for the above identified application. Charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

Peter K. McLarty

Peter K. McLarty
Agent for Applicants
Reg. No. 44,923

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